

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.

FIS920000404 (00750452AA)

In Re Application Of: Park et al.

JUL 22 2003

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GROUP 1700

Serial No.
09/924,318

Filing Date
08/08/2001

Examiner
B. Tran

Group Art Unit
1765

Title: **METHOD OF BUILDING A CMOS STRUCTURE ON THIN SOI WITH SOURCE/DRAIN ELECTRODES FORMED BY IN SITU DOPED SELECTIVE AMORPHOUS SILICON**

TO THE COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Response to Requirement for Restriction

in the above identified application.

- ☒ No additional fee is required.
- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. _____ as described below.
- ☐ Charge the amount of _____
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- ☒ Charge any additional fee required.

09-0458


Signature

Dated: July 22, 2003

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PATENT TRADEMARK OFFICE

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